

ECE350: Computer System Organization

Lecture Information

Time: M,W, F: 12:00pm to 12:50pm

CHE 2110

Instructor: Dr Ankur Srivastava,

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Office Hours: M,W 1:00pm to 2:00pm

Text Book: Computer Organization and Design: The Hardware Software Interface: D.A. Patterson and J.L. Hennessey

Class Url: TBA

Grading Policy

Homework and Projects: 15 %

2 Midterms OR 1 Midterm 1 Project: 25% Each

End Term: 35%

Exam:

- All exams will be closed book, closed notes, no calculators or PDAs, and please turn off the cell phones.
- **There will NOT be any make-up midterm exams.** If you have to miss a Mid-Term, then you must get Dr Srivastava's permission at-least 2 days before the exam. In that case your other midterm will be counted twice. If you do not take permission then you get a 0. If you miss both Mid Terms with Dr Srivastava's permission, you will be graded out of your finals. You get a 0 if no permission is taken at least 2 days in advance. In case you have to miss the finals (with Dr Srivastava's permission) then you will need to retake the exam again unless a different strategy is put in place after discussion with the instructor.
- Please contact Dr Srivastava within 1 week of the date of return if you contest your score in the mid-term. No changes will be made after this period.
- Check **final exam schedule** before enrolling for the course. Professor Charles Silio is offering another section.
- If any exam (especially the final exam) is scheduled on a **religious holiday** that you are compelled to observe and you must make arrangement to take the exam on a different date, please see Dr Srivastava about making these arrangements
- **Academic dishonesty will not be tolerated** including but not limited to cheating, fabrication, facilitating academic dishonesty, and plagiarism. Academic dishonesty in this class includes outright copying on homework; however, discussing homework problems and exchanging tips is permissible and also encouraged. If there are any take-home exams, discussing the material with anyone, inside or outside of the class, is considered academic dishonesty. Instances of academic dishonesty will be referred to Office of Judicial Programs.

Homework and Projects

- There will be several homeworks. Homework assignments will be posted on the course webpage and announced in the lecture, normally at least one week before the due date. Homework will be collected in class or recitations on the due date and the graded homework will be returned to you in the recitation sections.
- **Late homework will not be accepted.** If you must miss a lecture where a homework assignment is due, it is your responsibility to find a reliable person to turn your homework in for you or submit it to Dr. Srivastava or your recitation TA before the due date.
- Both effort and correctness will be counted when your homework is graded. It is important that you do the homework problems in the same order as they are assigned.
- If you dispute your score on any homework, you have to contact your recitation TA within one week from the date that your homework is officially returned (normally in recitation). If the matter remains unsettled, you have one more week to bring the issue to Dr. Srivastava with a written request.
- Make sure that you include the following information on the first page of your homework: full name, student ID, and your **recitation section number (on the upper right corner)**. Failure to do so will result in late grading of your homework, and you may consequently miss the one-week period to dispute your score.
- It is acceptable, and you are encouraged, to discuss homework problems with others, but you have to prepare the final write-up by yourself. Both copying homework and allowing others to copy your homework will be considered as academic dishonesty (see above in the last item in the Exam section).
- Some homeworks in this course will be programming assignments. The submission instructions for these will be appropriately elaborated in the homework document.

Broad Course Topics (subject to change)

MIPS Instruction Set Architecture

Computer Arithmetic

Processor Datapath and Control

Pipelining

Cache and Virtual Memory

Recitations

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